Ukpik-1

*DOCUMENT TITLE*

REVISION NUMBER: *0.1*

DATE: *YYYY-MM-DD*

COMPILED BY: *Name*

CONTRIBUTIONS FROM: *Names*

**Document Change Record**

|  |  |  |  |
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| Issue | Date | Changes Made | Name |
| 0.1 | YYYY-MM-DD | First Draft | Last Name, First Names |
|  |  |  |  |
|  |  |  |  |

**Reference Documents**

*Insert applicable reference document titles, such as requirements documents*

**Terms, Definitions, Abbreviations**

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| --- | --- |
| CSA | Canadian Space Agency |
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# Requirements

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| --- | --- | --- |
| **Requirement ID** | **Requirement Description** | **Parent Requirement** |
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# Architecture and Interface Diagrams

*Block diagram schematic to show internal interface connections and external interface connections. Label data connections with signal paths, connector types, protocols, etc. Label power connections with current flow paths, voltages, connector types, etc. Identify physical connection points with connector, screw type, adhesive type, etc.*

*Showcase the satellite's internal hardware layout including rework access and assembly sequence*

*Demonstrate the completion of all subsystem interconnect layout*

# Single Point Failures

*Identification and assessment of single-point failure mode*

# Interface Control

*Demonstrate the completion of the CubeSat Interface Control Document (ICD)*

# Verification and Compliance Matrix

*Provide a walk-through of the verification and compliance matrix from unit-to-spacecraft level*

*Provide verification method for each Nanoracks requirement*

*Requirement verification strategy: Take the subsystem requirements and identify how that requirement will be verified to have been met, and identify the necessary equipment or resources to complete that verifications*

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| --- | --- | --- |
| **Requirement ID** | **Verification Strategy** | **Parent Requirement ID** |
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# Schedule and Work Plan for Phase C2 and D

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| --- | --- | --- |
| **Task Description** | **Estimation of Time and Human Resources** | **Required Resources to Complete** |
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*Provide a description of the work that remains to be completed to complete the detailed design process. Provided an estimated time required to complete that work. Provide an estimate on the time and schedule for completing the testing, verification, assembly, and integration.*